

CZT

SPECIFICATION FOR APPROVAL

CUSTOMER _____

CUSTOMER'S PART NO. _____ REV. _____

DESCRIPTION **RJ45+2USB3.0 (10/100 Base-T 1 Port)**

CZT'S PART NO. **JBF111030004** REV. **A**

CZT OLD P/N **CZT-JBF1107-01**

ISSUE NO. **15B-088**

ISSUE DATE **MAR.27,2015**

☐ APPROVED

☐ REJECTED:

☐ APPROVED UNDER
THE CONDITIONS:

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Note:

- 1. This specification is the proprietary of Yihua Communicated Connector Co., Ltd. and is not allowed to be revealed to third party without any written approval .**
- 2. This specification is valid within 1 year before approval, and for any updated specification information required, please contact with CZT's sales window before order releases.**

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Customer Part No.: CZT Part No.: JBF111030004 Part Name: RJ45+2USB3.0 (10/100 Base-T 1 Port)					Rev.: A
Rev	Description	Drawn	Designed	Approved	Issue Date
A	New Release	Sunny Shi	/	DaVid Chen	MAR.27.2015

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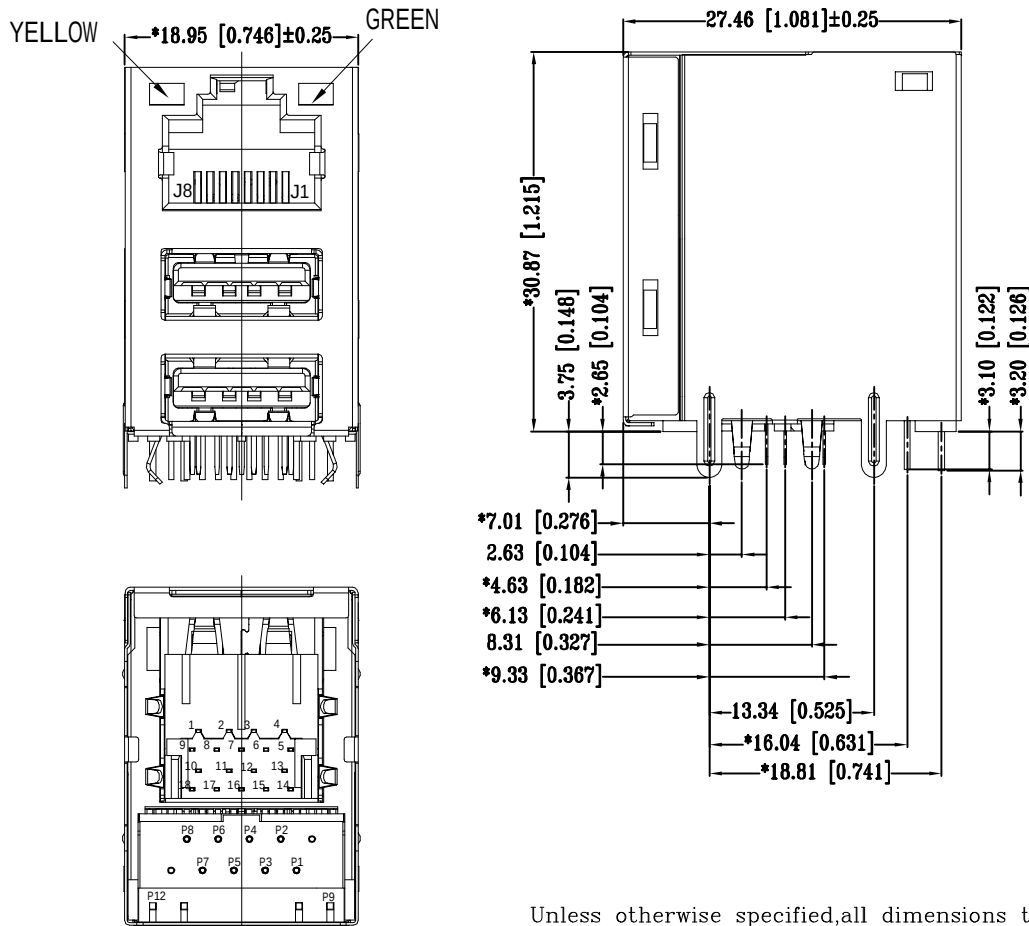
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CZT Part No.: JBF111030004
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Rev.: A



4. Mechanical Dimension and Notes:

4-1. Mechanical Dimension:



Unless otherwise specified, all dimensions tolerances is $\pm 0.254 / 0.010$.

4-2. Notes:

1. CONNECTOR MATERIAL:
 - 1.1 RJ45
 - HOUSING: THERMOPLASTIC UL94 V-0
 - SHIELD: Brass
 - SHIELD PLATING: NICKEL PLATING
 - CONTACT: COOPER ALLOY
 - CONTACT PLATING: SELECTIVE GOLD, 6 MICRO-INCHS MIN IN CONTACT AREA
 - 1.2 USB3.0
 - HOUSING: THERMOPLASTIC UL94 V-0
 - CONTACT: COPPER ALLOY
 - CONTACT PLATING: SELECTIVE GOLD, 6 MICRO-INCHS MIN IN CONTACT AREA
3. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL DRAWING FOR OMITTED PINS
4. RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68.
5. THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260° C MAX, 10 SECS MAX
6. OPERATING TEMPERATURE T=0° C TO +70° C.
7. STORAGE TEMPERATURE T=-40° C TO +85° C.
8. ALL CRITICAL DIMENSIONS WITH ***

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	565nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V

*WITH A FORWARD CURRENT OF 20mA

Drawn by

Designed by

Approved by

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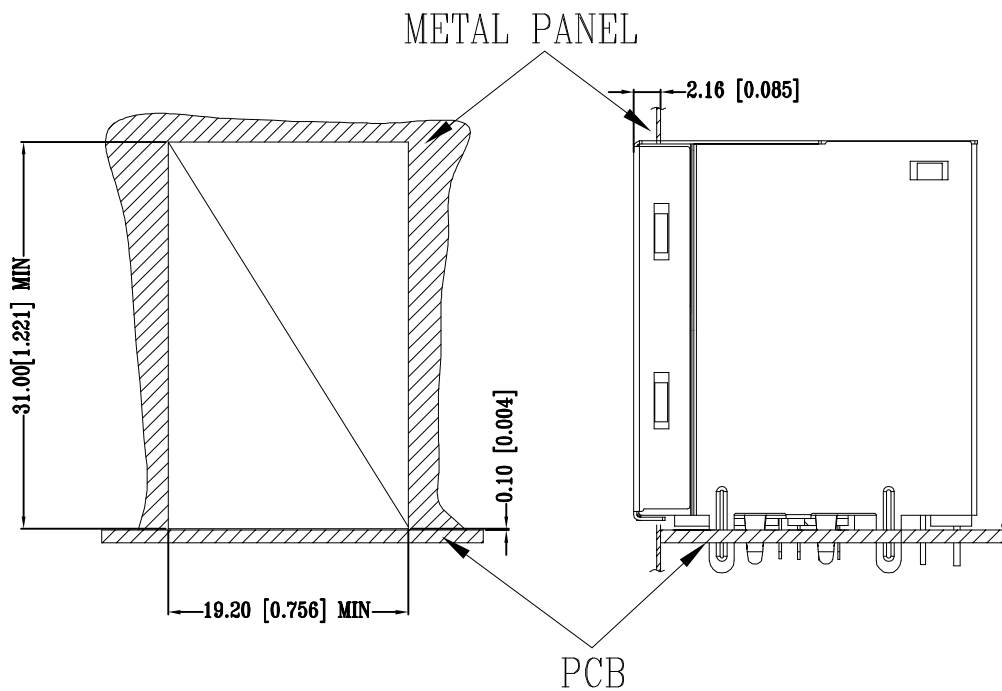
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5. Label and Recommended PWB Layout and Suggested Panel Opening:

5-1. Suggested Panel Opening:



UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

REFERENCE ONLY

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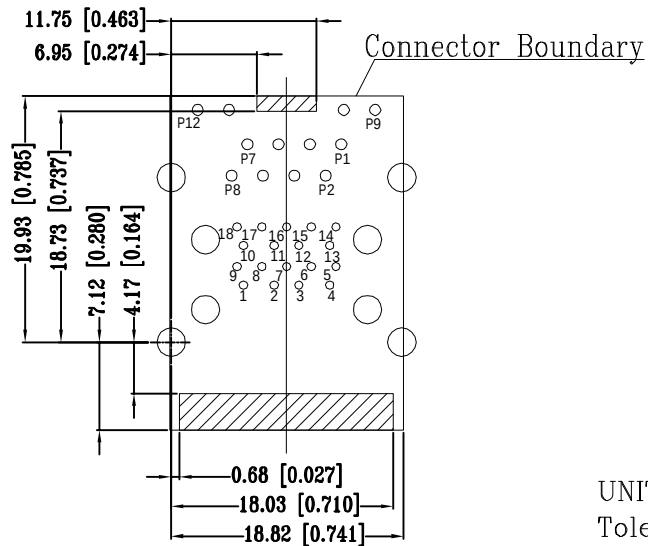
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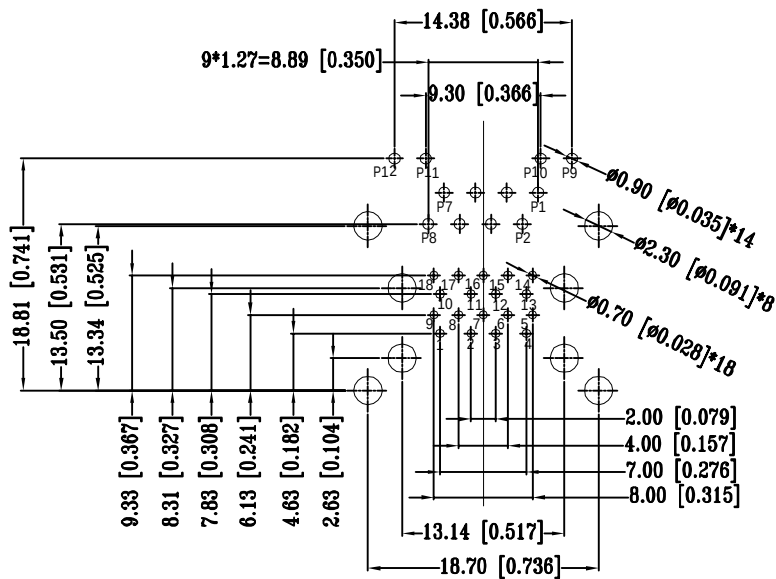


5-2. Keep-Out Area (Component Side View):



UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

5-3. Recommended PWB Layout (Component Side View):



UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

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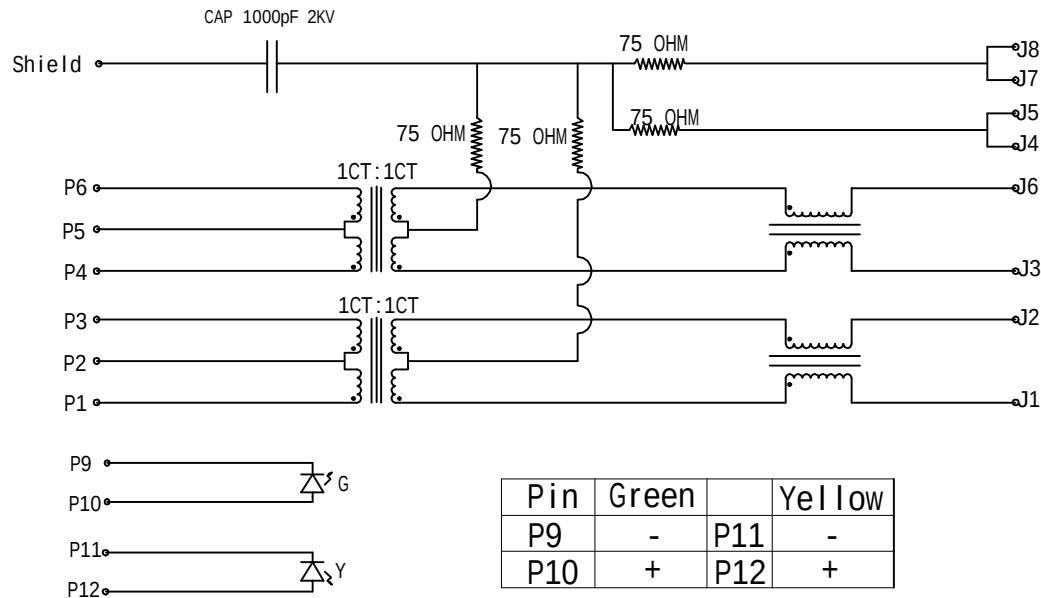
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6. Circuit Schematic and Electronical Specifications:

6-1. Circuit Schematic:



6-2. Electronical Specifications:

1.0 Turn Ratio @100KHz: (P1~P3):(J1~J2) = 1:1 $\pm$ 5%

$$(P4 \sim P6):(J3 \sim J6) = 1:1 \pm 5\%$$

2.0 Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS

3.0 DC Resistance:(J1~J2):(J3~J6) 1.2 OHMS MAX

4.0 Insertion Loss: 1-100MHz -1.2dB MAX

5.0 Return Loss:	1-30MHz	-16dB MIN
	30-60MHz	-12dB MIN
	60-80MHz	-10dB MIN

6.0 CROSS TALK: 1-100MHz -30dB MIN

7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN

8.0 Isolation: PHY Side to Line Side: 1500VAC OR 2250VDC