

CZT

SPECIFICATION FOR APPROVAL

CUSTOMER _____

CUSTOMER'S PART NO. _____ REV. _____

DESCRIPTION RJ45 ICM (10/100 Base-T 1 Port)

CZT'S PART NO. JDBU00690024 REV. A

CZT OLD P/N _____

ISSUE NO. 15B-325

ISSUE DATE Oct.14,2015

☐ APPROVED	☐ REJECTED:
☐ APPROVED UNDER THE CONDITIONS:	

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Note:

- 1. This specification is the proprietary of Yihua Communicated Connector Co., Ltd. and is not allowed to be revealed to third party without any written approval .**
- 2. This specification is valid within 1 year before approval, and for any updated specification information required, please contact with CZT's sales window before order releases.**

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Customer Part No.: CZT Part No.: JDBU00690024 Part Name: RJ45 ICM (10/100 Base-T 1 Port)					Rev.: A
					 RoHS
Rev	Description	Drawn	Designed	Approved	Issue Date
A	New Release	Sunny shi	/	DaVid Chen	Oct,14,2015

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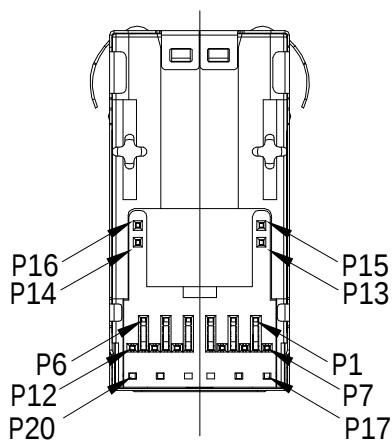
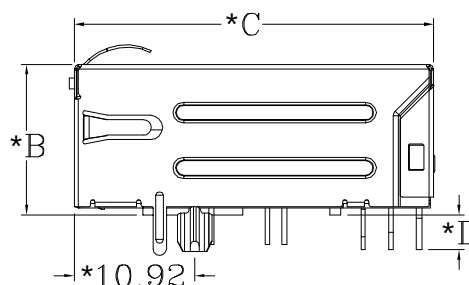
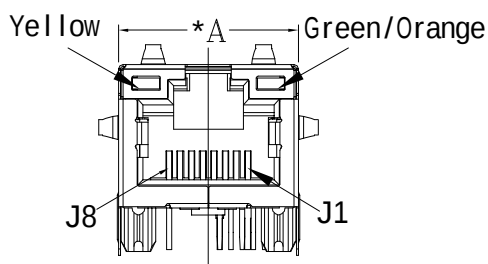
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CZT Part No.: JDBU00690024
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Rev.: A



4. Mechanical Dimension and Notes:



UNIT : mm / inch

A = 16.31 / 0.642

B = 13.61 / 0.536

C = 32.57 / 1.282

D = 3.17 / 0.125

Unless otherwise specified, all dimensions tolerances is ± 0.2 / 0.008.

4-2. Notes:

1. CONNECTOR MATERIAL:
HOUSING: PA66 BLACK UL94 V-0
INSERTER: PBT BLACK UL94 V-0
SHIELD PLATING: NICKEL PLATING
CONTACT: COOPER ALLOY
CONTACT PLATING: SELECTIVE GOLD, 6 MICRO-INCHES MIN IN CONTACT AREA
2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED SEE ELECTRICAL DRAWING FOR OMITTED PINS
3. RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68.
4. THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260° C MAX, 10 SECS MAX
5. OPERATING TEMPERATURE T=0° C TO +70° C.
6. STORAGE TEMPERATURE T=-40° C TO +85° C.
7. ALL CRITICAL DIMENSIONS WITH ""

LED SPECIFICATION			
STANDARD LED	WAVELENGTH	Forward V(max)	TYP
GREEN	565nm	2.4V	2.2V
YELLOW	590nm	2.5V	2.1V
ORANGE	625nm	2.5V	2.0V

*WITH A FORWARD CURRENT OF 20mA

Drawn by

Designed by

Approved by

CZT

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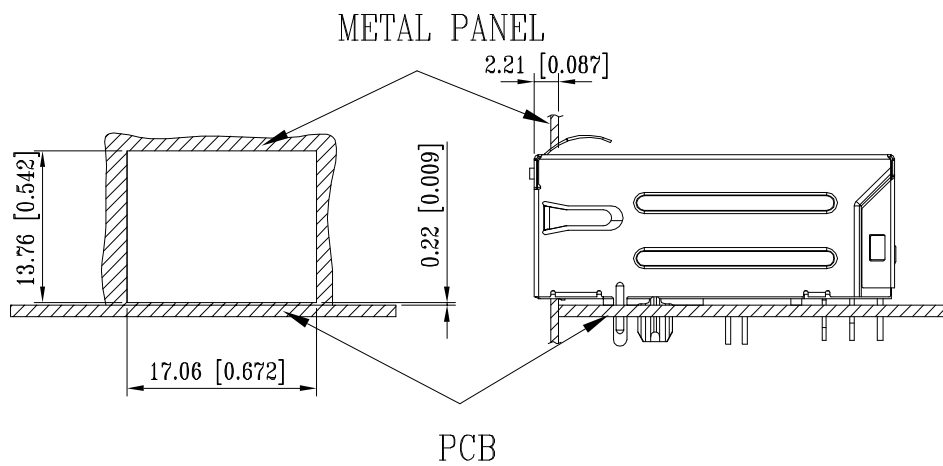
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5. Label and Recommended PWB Layout and Suggested Panel Opening:

5-1. Suggested Panel Opening:



UNIT: mm / inch
Tolerances: ± 0.10 / 0.004

REFERENCE ONLY

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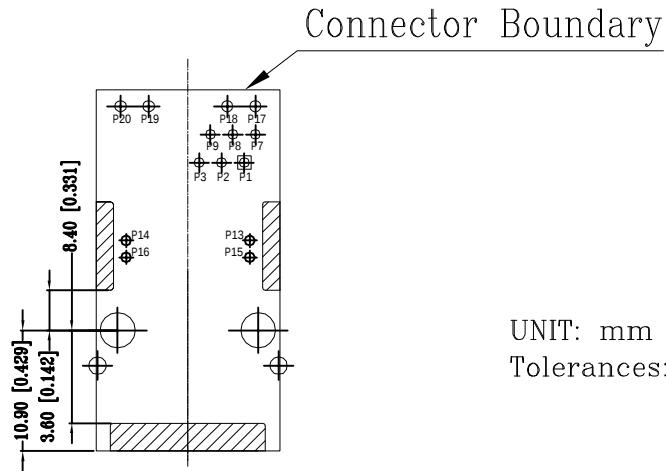
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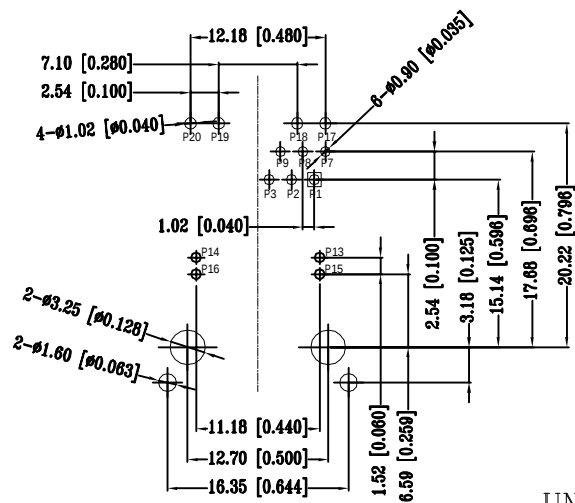


5-2. Keep-Out Area (Component Side View):



UNIT: mm / inch
 Tolerances: ± 0.10 / 0.004

5-3. Recommended PWB Layout (Component Side View):



UNIT: mm / inch
 Tolerances: ± 0.10 / 0.004

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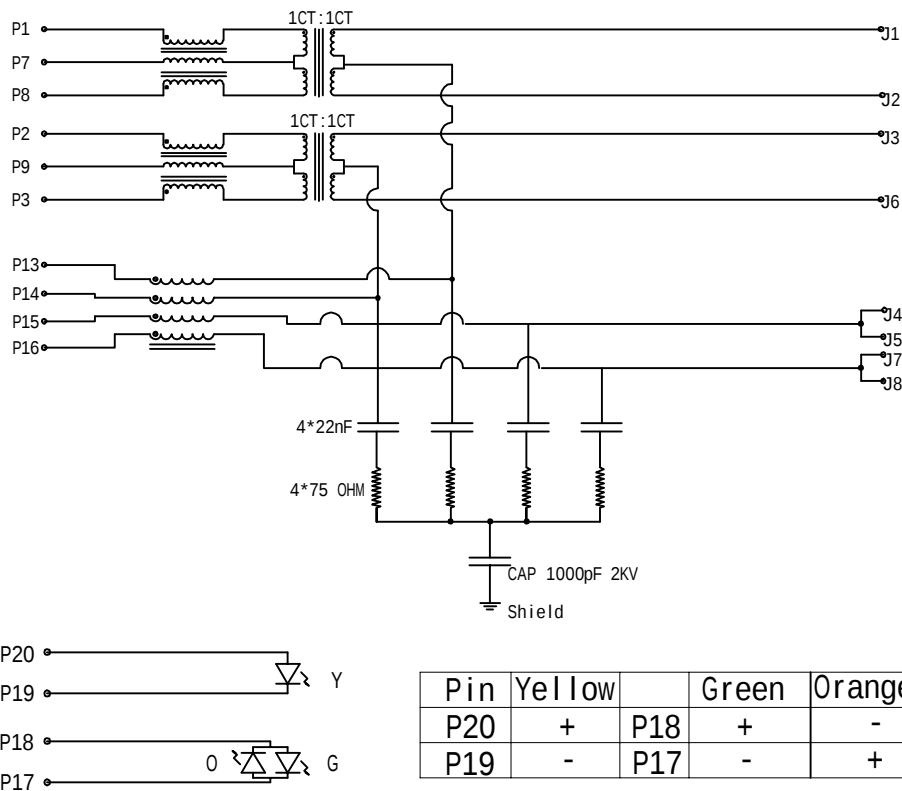
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6. Circuit Schematic and Electrical Specifications:

6-1. Circuit Schematic:



6-2. Electrical Specifications:

- 1.0 Turn Ratio @100KHz: (P1~P8):(J1~J2) = 1:1±5%
 (P2~P3):(J3~J6) = 1:1±5%
- 2.0 Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS
- 3.0 DC Resistance:(J1~J2):(J3~J6) 1.2 OHMS MAX
- 4.0 Insertion Loss: 1-100MHz -1.2dB MAX
- 5.0 Return Loss: 1-30MHz -16dB MIN
 30-60MHz -12dB MIN
 60-80MHz -10dB MIN
- 6.0 CROSS TALK: 1-100MHz -30dB MIN
- 7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
- 8.0 Isolation: PHY Side to Line Side: 500VAC
- 9.0 DC CURRENT/VOLTAGE RATING-PSE PINS: 350mA @57V(CONTINUOUS)